

This listing of claims will replace all prior versions, and listings, of claims in
the application

LISTING OF CLAIMS

1. (Cancelled).

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2. (Currently Amended) The circuit according to claim 7 4, further
comprising:

a diplexer;

10 wherein a transmission band and a reception band of a transmission system
 form a band pair, a frequency difference between band pairs of a first
 and of a second transmission system amounts to approximately one
 octave, said diplexer being arranged between said common antenna
 and said filters for distinguishing between said band pairs.

15 3-4. (Canceled).

5. (Currently Amended) The circuit according to claim 7 4, further comprising
a low pass filter as a transmission filter.

20 6. (Cancelled).

7. (Previously Presented) A front-end circuit for a multi-mode communication
terminal device, comprising:

25 at least one switch element selected from the group consisting of RF
 switches, duplexers and diplexers;

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a common antenna;

a first transmission system being a pure mode transmission system
configured to operate in a pure TDD mode;

a second transmission system being a pure mode transmission system
configured to operate in a pure FDD mode;

filters provided for said first and second transmission systems;

a connecting circuit via which individual filters of said filters are connected to
said common antenna, said connecting circuit comprising said at least
one switch element;

said filters comprising a first filter, a second filter, and a third filter, said first
filter being a transmit filter of said FDD system, said second filter being
a common receive filter for said TDD system and said FDD system,
and said third filter being a transmit filter for said TDD system;

a duplexer formed by said first filter and said second filter; and

a switch element comprising an RF switch to connect said common antenna
with one of said duplexer and said third filter.

8. (Previously Presented) The circuit according to claim 7, further comprising:

an RF switch between a common transmission path for said pure FDD mode
transmission system and said pure TDD mode transmission system
and two transmission filters; and

an RF multiple switch at said antenna for switching between a duplexer for
said FDD mode, a transmission filter and a reception filter for said TDD
mode;

frequency bands of said mixed mode transmission system being clearly
spaced from one another.

9-11. (Cancelled).

12. (Currently Amended) The circuit according to claim 7 4, wherein said
5 switches are fashioned as GaAs FET transistors.

13. (Currently Amended) The circuit according to claim 7 4, wherein said
switches are realized with PIN diodes having additional phase shifters.

10 14. (Cancelled).

15. (Currently Amended) The circuit according to claim 7 4, wherein
individual components of the circuit are arranged in a discrete manner on a common
printed circuit board.

15 16. (Currently Amended) The circuit according to claim 7 4 wherein at least a
part of discrete components of said circuit is integrated in a common substrate.

17. (Original) The circuit according to claim 16, wherein all individual
20 components together with a DC drive are integrated in a common substrate that is
realized in a multi-layer technique with partially planar structures.

18. (Currently Amended) The circuit according to claim 7 4, further
comprising a directional coupler for regulating power of a power amplifier as part of
25 a detector of at least one transmission input.

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19. (Currently Amended) The circuit according to claim 7 4, further comprising a protective element that protects a transmission amplifier against feedback or reflected power and is selected from a group consisting of an insulator and a circulator, and is arranged between a transmission amplifier and a
5 transmission filter.

20-22. (Canceled).

23. (Currently Amended) The circuit according to claim 7 22, further
10 comprising an RF switch between a common transmission path for said pure FDD mode transmission system and said pure TDD mode transmission system and two transmission filters.

24-27. (Canceled).

28. (Previously Presented) The circuit of claim 7, wherein frequency bands of said first and second transmission systems are overlapping or adjacent to each other.

29. (Currently Amended) The circuit according to claim 7 4, wherein said
20 switch is a duplexer, wherein said duplexer is realized as an independent component and comprises at least one filter selected from the group consisting of a SAW filter, an MWK filter, an FBAR filter, a strip-line filter, and an LC-filter.

30. (Previously Presented) The circuit according to claim 7, further
25 comprising:

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a common transmit path for said TDD system and said FDD system; and

a further RF switch to connect said common transmit path with one of said
second filter and said third filter.

5 31-36. (Cancelled).